

Pradeep Elamanchili, Vice President of Engineering leads Western Digital's Global ASIC organization, responsible for all ASIC silicon development with R&D sites across the world.

He brings to bear more than 2 decades of deep expertise in all areas of semiconductor product development. Holding a master's degree in electrical engineering, he started his professional career at Intel Corporation as a circuit design engineer and has since held various technical lead, management and architect positions developing low power processors (x86[®]™ and ARM[™]®) and SOCs for the mobile, networking, consumer and graphics/memory controller chipset divisions.

While at Advanced Micro Devices he led multiple SOC development teams developing Discrete Graphics Processors and integrated Gfx+CPU SOCs. He was instrumental in introducing the worlds 1st integrated APU[®] (GPU+ CPU) to the market. Prior to joining SanDisk, he served as Vice President of Silicon Engineering, responsible for all aspects of SOC development and product engineering at Ineda Systems. Outside professional work, Pradeep enjoys running, playing a wide range of sports, likes volunteering and serves on the board of a non-profit educational organization for the under-privileged in India.